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## REPLY UNDER 37 CFR §1.116 EXPEDITED PROCEDURE TECHNOLOGY CENTER 2800

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	§ Attorn	ey Docket No.:
Chi-Chun Chen, et al.	§	2002-0066 / 24061,461
Serial No.: 10/600,393	§ Custor	ner No. 42717
Filed: June 20, 2003	s Group	Art Unit: 2822
For: METHOD OF FORMING DUAL GATE INSULATOR LAYERS FOR	§ Exami §	ner: Toniae M. Thomas
CMOS APPLICATIONS	•	mation No.: 8529

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Kelly A. Moredu

Sir:

## AMENDMENT UNDER 37 C.F.R. 81.116

This Amendment is submitted in reply to the Office Action mailed on March 25, 2005. (Please see the pages that follow).